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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/587,887	07/27/2006	Theodore W. Jochum	P155US02	2357
Thomas F Coon	7590 04/16/200 ney	EXAMINER		
Dako Global IP	•	KUNDU, SUJOY K		
Dako Colorado Inc 4850 Innovation Drive			ART UNIT	PAPER NUMBER
Fort Collins, CO 80525			2863	
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			04/16/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)		
	10/587,887	JOCHUM ET AL.		
Office Action Summary	Examiner	Art Unit		
	SUJOY K. KUNDU	2863		
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the c	orrespondence address		
A SHORTENED STATUTORY PERIOD FOR REPL' WHICHEVER IS LONGER, FROM THE MAILING D. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be time will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).		
Status				
Responsive to communication(s) filed on 27 Ju This action is FINAL . 2b) ☐ This Since this application is in condition for alloware closed in accordance with the practice under E	s action is non-final. nce except for formal matters, pro			
Disposition of Claims				
4) ☐ Claim(s) 1-18 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-18 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/o Application Papers 9) ☐ The specification is objected to by the Examine 10) ☐ The drawing(s) filed on 27 July 2006 is/are: a) Applicant may not request that any objection to the	wn from consideration. or election requirement. er. □ accepted or b)⊠ objected to b	•		
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	tion is required if the drawing(s) is obj	ected to. See 37 CFR 1.121(d).		
Priority under 35 U.S.C. § 119				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 				
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date 12/14/2007.	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ate		

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DETAILED ACTION

Priority

Acknowledgment is made of applicant's claim for foreign priority under 35
 U.S.C. 120/371.

2. Applicant does not claim priority in the written description. Appropriate Action is required.

Drawings

With regards to Claims 1, 3, 4, 7, and 8, numbered boxes do not convey to one of ordinary skill the mature of what is being depicted and thereby aid in the understanding of the invention 37 CFR 1.81(a,b) or what is claimed 37 CFR 1.83(a), applicant is required to use suitable descriptive title legends that reflect the function/purpose of the depicted feature 37 CFR 1.81(c), 37 CFR 1.83(c), 37 CFR 1.84(o).

Specification

The abstract of the disclosure does not commence on a separate sheet in accordance with 37 CFR 1.52(b)(4). A new abstract of the disclosure is required and must be presented on a separate sheet, apart from any other text.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Art Unit: 2863

Claims 1-8, and 10-17 are rejected under 35 U.S.C. 102(b) as being anticipated by Riley et al. (6,507,391).

With regards to Claim 1, 10, Riley teaches a method for measuring and analyzing data contained within pulses of an analog electronic signal derived from optical measurements in a flow cytometer, the electronic signal comprising a first data channel, the method characterized by comprising the steps of:

- (a) removing a DC offset from the signal with a base line restoration circuit (Figure 6, Column 16, Lines 16-20);
- (b) transforming the signal with a logarithmic amplifier (Figure 6, Column 16, Lines 13-16);
- (c) sampling the transformed signal with an analog-to-digital converter so as to produce a digital signal (figure 6, Column 16, Lines 16-22); and
- (d) analyzing the digital signal with an electronic processor (Column 16, Lines 22-23).
- . With regards to Claim 2, 11, Riley teaches the method, wherein the processor performs peak sample and hold analysis upon the digital signal (Column 16, Lines 20-23).

With regards to Claim 3, 12, Riley teaches the method, wherein the processor further analyzes a second digital signal comprising a second data channel of the flow cytometer (Column 16, Lines 41-53)

With regards to Claim 4, 13, Riley teaches the method, wherein the DC offset is locked during pulses of the electronic signal (Column 22, Lines 61-67).

With regards to Claim 5, 14, Riley teaches the method, characterized by comprising the further step, between the transforming step (b) and the sampling step (c) of calibrating a gain of the transformed signal (Column 16, Lines 12-40).

With regards to Claim 6, 15, Riley teaches the method, characterized by comprising the farther steps of:

- (e) controlling a digital-to-analog converter based upon the signal analysis performed by the processor (Figure 6, Column 16, Lines 12-40); and
- (f) inputting a DC voltage from the digital-to-analog converter to the baseline restoration circuit (Figure 6, Column 16, Lines 12-40).

With regards to Claim 7, 16, Riley teaches the method, wherein the processor calibrates for errors in the transformed signal output of the logarithmic amplifier (Figure 6, Column 16, Lines 13-16).

With regards to Claim 8, 17, Riley teaches the method, wherein the calibration is performed by means of a lookup table for correcting output values of the analog-to-digital converter (Column 25, Lines 37-44).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 9 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Riley et al. (6,507,391) in view of Linz (5,598,158).

With regards to Claims 9 and 18, Riley fails to teach the limitation of the method, wherein the analog-to-digital converter samples at a lower bit resolution than is required to analyze the signal prior to the transforming step (b).

Linz teaches the limitation of the method, wherein the analog-to-digital converter samples at a lower bit resolution than is required to analyze the signal prior to the transforming step (b).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to include the limitation of the method, wherein the analog-to-digital converter samples at a lower bit resolution than is required to analyze the signal prior to the transforming step (b) as taught by Linz into Riley for the purpose of synchronizing all samples of data prior to correcting and detecting.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to SUJOY K. KUNDU whose telephone number is (571)272-8586. The examiner can normally be reached on M-F 9-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Barlow can be reached on 571-272-2269. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/S. K. K./ Examiner, Art Unit 2863 /Edward R. Cosimano/ Primary Examiner, Art Unit 2863